



Verification of Translation

I, Takashi Noyama, maintaining my place of business at IKEUCHI·SATO & PARTNER PATENT ATTORNEYS, OAP Tower 26F, 8-30 Tenmabashi, 1-chome, Kita-ku, Osaka-shi, OSAKA 530-6026, Japan, hereby certify that the attached pages of English text are a true and correct translation of the Japanese language patent application filed on December 29, 2003, entitled METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE and assigned Serial No. 10/750,359.

I additionally attest that I have knowledge of both the Japanese and the English languages and that I am further qualified by education, experience and vocation to make this verification. I affirm under the penalty of perjury under the laws of the United States that the foregoing is correct to the best of my information and belief.

Takashi Noyama
Name: Takashi NOYAMA

June 28, 2004
Date